

Features and Benefits

- 3.5V to 24V Operation
- -40°C to 150°C Superior temperature operation
- CMOS technology
- Low current consumption
- Chopper-stabilized amplifier stage
- Open drain output
- Low sensitivity
- Small Size-SOT23 3L and SIP 3L
- both RoHS compliant packages

Application Examples

- Automotive, Consumer and Industrial
- Solid-state switch
- Brushless DC motor commutation
- Speed detection
- Angular position detection
- Proximity detection
- Current detector

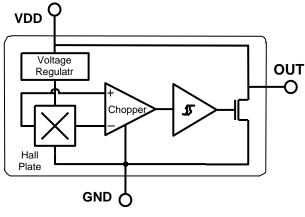


3 pin SOT23 (suffix SE)



3 pin SIP (suffix UA)

Functional Block Diagram



 $\begin{array}{ll} \text{UA Package} & \text{SE Package} \\ \text{Pin 1} - \text{V}_{\text{DD}} & \text{Pin 1} - \text{V}_{\text{DD}} \\ \text{Pin 2} - \text{GND} & \text{Pin 2} - \text{OUT} \\ \text{Pin 3} - \text{OUT} & \text{Pin 3} - \text{GND} \end{array}$



General Descriptions

The S5781 is a unipolar Hall-effect sensor IC fabricated from CMOS technology. The device integrates a voltage regulator, Hall sensor with dynamic offset cancellation system, small signal amplifier, Schmitt trigger and an open-drain output driver. With suitable output pull up, they can be used with bipolar or CMOS logic circuits.

These Hall-effect switches are monolithic integrated circuits with tighter magnetic specifications, designed to operate continuously over extended temperatures to $+150^{\circ}$ C, and are

more stable with both temperature and supply voltage changes. The unipolar switching characteristic makes these devices ideal for use with a simple bar or rod magnet.

Thanks to its wide operating voltage range and extended choice of temperature range, it is quite suitable for use in automotive, industrial and consumer applications.

The device is delivered in a Small Outline Transistor (SOT) and in a Plastic Single In Line (SIP 3L flat). Both 3-lead packages are RoHS compliant.

Glossary of Terms

MilliTesla (mT), Gauss Units of magnetic flux density: 1mT = 10 Gauss

RoHS Restriction of Hazardous Substances

Operating Point (B_{OP}) Magnetic flux density applied on the branded side of the package which turns the output

driver ON $(V_{OUT} = V_{DSon})$

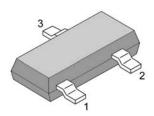
Release Point (B_{RP}) Magnetic flux density applied on the branded side of the package which turns the output

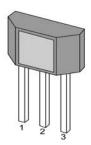
driver OFF ($V_{OUT} = high$)



Pin Definitions and Descriptions

| SOT Pin № | SIP Pin № | Name | Туре | Function |
|-----------|-----------|------|--------|-----------------------|
| 1 | 1 | VDD | Supply | Supply Voltage pin |
| 2 | 3 | OUT | Output | Open Drain Output pin |
| 3 | 2 | GND | Ground | Ground pin |





Absolute Maximum Ratings

| Parameter | Symbol | Value | Units |
|---------------------------|--------|------------|-------|
| Supply Voltage | VDD | 28 | V |
| Supply Current | IDD | 50 | mA |
| Output Voltage | VOUT | 28 | V |
| Output Current | IOUT | 50 | mA |
| Storage Temperature Range | TS | -65 to 170 | °C |

Absolute maximum ratings

| Operating Temperature Range | Symbol | Value | Units |
|-----------------------------|--------|------------|-------|
| Temperature Suffix "E" | TA | -40 to 85 | °C |
| Temperature Suffix "L" | TA | -40 to 150 | °C |

Exceeding the absolute maximum ratings may cause permanent damage. Exposure to absolute-maximum- rated conditions for extended periods may affect device reliability.



General Electrical Specifications

DC Operating Parameters T $_{A}~=25\,^{\circ}\!\text{C},\,V_{DD}\!\!=3.5V$ to 24V (unless otherwise specified)

| Parameter | Symbol | Test Conditions | Min | Тур | Max | Units |
|---|--------|-----------------------------|------|------|-----|-------|
| Supply Voltage | VDD | Operating | 3.5 | | 24 | V |
| Supply Current | IDD | $B < B_{RP}$ | 1.5 | 2.5 | 5 | mA |
| Output Saturation Voltage | VDSon | $IOUT = 20mA, B > B_{OP}$ | | 0.4 | 0.5 | V |
| Output Leakage Current | IOFF | $B < B_{RP} VOUT = 24V$ | | 0.01 | 10 | μА |
| Output Rise Time tr $RL = 1k\Omega$, $CL = 20pF$ | | | 0.25 | | μs | |
| Output Fall Time | tf | RL =1k Ω , CL = 20pF | | 0.25 | | μs |

Magnetic Specifications

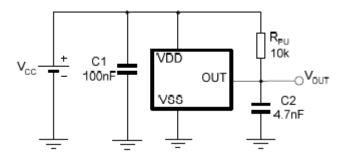
DC Operating Parameters $V_{DD} = 3.5V$ to 24V (unless otherwise specified)

S5781

| Parameter | Symbol | Test Conditions | Min | Тур | Max | Units |
|-----------------|------------------|--------------------|-----|-----|-----|-------|
| Operating Point | B _{OP} | | 70 | | 150 | G |
| Release Point | B_{RP} | Ta=25°C,Vdd=12V DC | 20 | | 100 | G |
| Hysteresis | B _{HYS} | | 20 | | 70 | G |

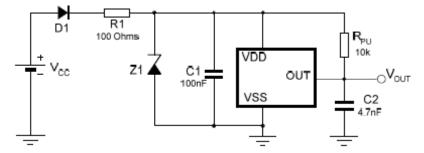
Application Information

Typical Three-Wire Application Circuit





Automotive and Harsh, Noisy Environments Three-Wire Circuit



Application Comments

For proper operation, a 100nF bypass capacitor should be placed as close as possible to the device between the VDD and ground pin. For reverse voltage protection, it is recommended to connect a resistor or a diode in series with the VDD pin.

When using a resistor, three points are important:

- the resistor has to limit the reverse current to 50mA maximum (VCC / R1 50mA)
- the resulting device supply voltage VDD has to be higher than VDD min (VDD = VCC R1.IDD)
- the resistor has to withstand the power dissipated in reverse voltage condition (PD = $VCC^2/R1$)

When using a diode, a reverse current cannot flow and the voltage drop is almost constant (0.7V).

Therefore, a 100 /0.25W resistor for 5V application and a diode for higher supply voltage are recommended.

Both solutions provide the required reverse voltage protection.

When a weak power supply is used or when the device is intended to be used in noisy environment, it is recommended that figure 13.3 from the Application Information section is used.

The low-pass filter formed by R1 and C1 and the zener diode Z1 bypass the disturbances or voltage spikes occurring on the device supply voltage VDD. The diode D1 provides additional reverse voltage protection.

ESD Precautions

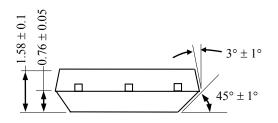
Electronic semiconductor products are sensitive to Electro Static Discharge (ESD).

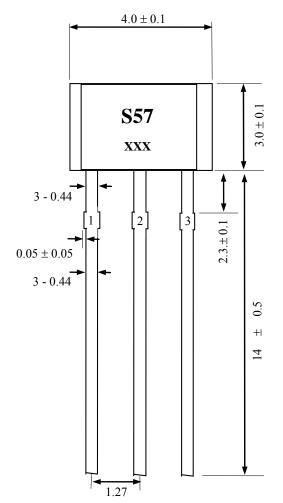
Always observe Electro Static Discharge control procedures whenever handling semiconductor products.

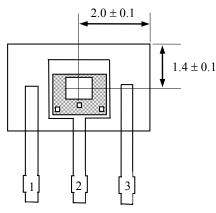


Package Information

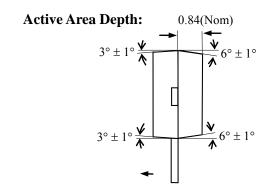
Package UA, 3-Pin SIP:







Hall plate Location



Notes:

- 1). Controlling dimension: mm;
- 2). Leads must be free of flash and plating voids;
- 3). Do not bend leads within 1 mm of lead to package interface;
- 4). PINOUT: Pin 1 VDD
 - Pin 2 GND Pin 3 Output

Marking:

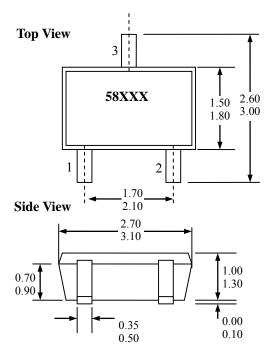
S --- SEC logo

57 --- Code of Device;

XXX -- Production Lot;



Package SE, 3-Pin SOT-23:



Notes:

1). PINOUT: Pin 1 VDD

Pin 2 Output

Pin 3 GND

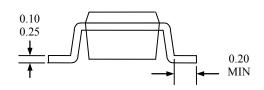
2). All dimensions are in millimeters;

Marking:

58 -- Code of Device (58 or 44);

XXX -- Production Lot;

End View



Ordering Information

| Part No. | Pb-free | Temperature Code | Package Code | Packing |
|----------|---------|--------------------|--------------|------------------------------|
| S5781 | YES | E (-40°C to 85°C) | SE(SOT-23) | 7-in. reel, 3000 pieces/reel |
| S5781 | YES | E (-40°C to 85°C) | UA(TO-92) | Bulk, 1000 pieces/bag |
| S5781 | YES | L (-40°C to 150°C) | SE(SOT-23) | 7-in. reel, 3000 pieces/reel |
| S5781 | YES | L (-40°C to 150°C) | UA(TO-92) | Bulk, 1000 pieces/bag |